

**MAR 01 2005**

Our Reference: 2024.47

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lowrey, et al

Application Number: 10/801,414

Filing Date: March 16, 2004

Art Unit/Examiner: 2814/Pham, Hoai V.

Title: METHOD FOR MAKING PROGRAMMABLE RESISTANCE MEMORY  
ELEMENT

**CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed in this correspondence is:

1. Response to Office Action of December 1, 2004. - 10 pages

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office,

Fax Number 703 872-9306 on MARCH 1, 2005

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Date: MARCH 1, 2005

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AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This is a response to the Office Action dated December 1, 2004 to which a response is due on or before March 1, 2005. The Commissioner is hereby authorized to charge any fees which may be required during the entire pendency of the application to Deposit Account No. 05-1068.